

**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Attorney Docket No. 1054.022

In re application of: Phillip Johnson, Gary Powell and William Andrews

Serial No.: Not Yet Assigned  
Filed: Not Yet Assigned

Group No.: Not Yet Assigned  
Examiner: Not Yet Assigned

For: Programmable Lock Detector And Corrector

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 CFR § 1.56, this is an Information Disclosure Statement (IDS) for the above-captioned U.S. patent application. An IDS Form PTO-1449 listing references is enclosed herewith and forms a part hereof.

Certification Under 37 CFR § 1.704(d)

- ☐ Each item of information contained in this statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this statement.

Certification Under 37 CFR § 1.97(e)

- ☐ Each item of information contained in this statement was either (1) cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement or (2) not known to any individual designated in § 1.56(c) more than three months prior to the filing of this statement.

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Certification Under 37 CFR 1.10

"Express Mail" Mailing Label No. EV140157962US

Date of Deposit July 28, 2003

I hereby certify that this paper or fee is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Mary E. Caniz  
(Name of person mailing)

Mary E. Caniz  
(Signature of person mailing)

Fee Under 37 CFR § 1.97(c)

☐ The fee for submission of this IDS under § 1.97(c) is \$180.00 as set forth in § 1.17(p).

Petition Under 37 CFR § 1.97(d)

☐ This is a petition requesting consideration of an IDS under § 1.97(d). The petition fee for submission of this IDS under § 1.97(d) is \$130.00 as set forth in § 1.17(i).

Provision of Copies of References

☒ A copy of each of the listed references is enclosed herewith and forms a part hereof.

☐ A translation of each of Reference(s)     is enclosed herewith and forms a part hereof.

☐ A copy of the International Search Report from a corresponding PCT application is enclosed herewith.

☐ A copy of the European Search Report from a corresponding EPO application is enclosed herewith.

☐ The above-captioned U.S. patent application is a

- ☐ continuation
- ☐ divisional
- ☐ continuation-in-part

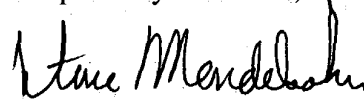
application of U.S. application serial number       , filed on        as Attorney Docket No.       . Each of the listed references was previously cited by or submitted to the Office in the prior application. The above-captioned U.S. patent application relies upon the prior application for an earlier filing date under 35 U.S.C. 120. Pursuant to § 1.98(d), copies of the listed references are not being provided with this IDS.

Authorization To Charge Deposit Account

☒ Authorization is given to charge **Mendelsohn & Associates, P.C. Deposit Account No. 50-0782** for any fee under § 1.97(c) and § 1.17(p) or under § 1.97(d) and § 1.17(i).

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449 form as confirmation that each of the documents has been considered.

Respectfully submitted,



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Date: 7/28/03

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Enclosures

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE  
(Rev. 2-32) PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

(Use several sheets if necessary)

ATTY. DOCKET NO.

**SERIAL NO.**

~~1054.022~~  
~~APPLICANT~~

Not yet assigned

Phillip Johnson, Gary Powell and William Andrews	
FILING DATE	GROUP
11/1/78	1
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Not yet assigned

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[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AA	"CMOS DLL Based 2V, 2.3ps Jitter, 1GHz Clock Synthesizer and Temperature-Compensated Tunable Oscillator," by David J. Foley, <i>Student Member, IEEE</i> , and Michael P. Flynn, <i>Senior Member, IEEE</i> .	

EXAMINER	DATE CONSIDERED
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**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449)